

## Coder 5.04-5 Release Notes

### DATE:

August 31, 2018

### BACKGROUND:

Version 5.04-5 is an emergency release of the GAP Programmer for the MicroNet and MicroNet TMR control. This release is made from 5.04-4 address's the following issues.

### COMPATABILITY:

**This version of the GAP Programmer may only be used for MicroNet Plus and MicroNet TMR controls. Atlas, Engine, easYgen and Cyber MicroNet Plus controls are not supported**

To use this version of GAP/Coder, you must use GAP 2.18F or higher. *GAP 3.09 is recommended.*

To use this version of GAP/Coder, you must use SOS 4.01 or higher.

To use this version of GAP/Coder, you must use Control Assistant 4.00 or higher.

To use this version of GAP/Coder, you must use AppManager 3.07 or higher.

To use this version of GAP/Coder, you must use Toolkit 3.5 update 3 or higher.

To use this version of GAP/Coder, you must use Windows XP®, Windows Vista®, Windows 7® or Windows 8®.

### NEW FEATURES:

#### ℞• Issue solutions only

#### Issues:

The following issues and requests from Product Issue Database ("PID")/Redmine bugs were addressed in 5.04-5:

Bugs#	Description	Solution
56097	AIO_COMBO module drops out during a failover with reason 0x4000	Fixed the execution order of key generation, FPGA watchdog tickle and key broadcast to the IO modules to handle the failover when masterclockprocess takes an exception.
59176, 58986, 58766, 58752, 58657	{PID#18494}Valve Unpredictable When SD=RST=TRUE SD description for DIG_RVLV Block Help Incorrect / Misleading  {PID#17917}CALC_PLUS chokes on "OUT" variable name in NetSim compile  {PID#16910}Simplot Redundant ACT CTRL CAL  {PID#16860}Can't fully tune BAUD on	help fixes

Bugs#	Description	Solution
	SIO232PORT and SIO_PORT  {PID#16187}Bad SID File, HMI_ENUM block problem	

## UNSUPPORTED BLOCKS:

The following blocks are available in the Template and GAP Editor, but are not supported for customer use in the 5.04-3 release version of the Coder (compiler):

- [CAL\\_ID](#)
- [I\\_TO\\_NAN](#)
- [PC104\\_ATL](#)
- [TEMP\\_HIST](#)

## PART NUMBERS:

Version 5.04-5  
Gap/Coder Part Number: 9927-2131

## Coder 5.04-4 Release Notes

### DATE:

August 21, 2015

### BACKGROUND:

Version 5.04-4 is an emergency release of the GAP Programmer for the MicroNet and MicroNet TMR control. This release is made from 5.04-3 address's only one issue. This release is going to use the coder prebuilt libraries for 5.04-3 since the issue for the fix is not library dependent

### COMPATABILITY:

**This version of the GAP Programmer may only be used for MicroNet Plus and MicroNet TMR controls. Atlas, Engine, easYgen and Cyber MicroNet Plus controls are not supported**

To use this version of GAP/Coder, you must use GAP 2.18F or higher. *GAP 3.09 is recommended.*

To use this version of GAP/Coder, you must use SOS 4.01 or higher.

To use this version of GAP/Coder, you must use Control Assistant 4.00 or higher.

To use this version of GAP/Coder, you must use AppManager 3.07 or higher.

To use this version of GAP/Coder, you must use Toolkit 3.5 update 3 or higher.

To use this version of GAP/Coder, you must use Windows XP®, Windows Vista®, Windows 7® or Windows 8®.

### NEW FEATURES:

- **Issue solutions only**

### ISSUES:

The following Product Issue Database ("PID") issues and requests were addressed in 5.04-3:

Issue #	Description	Solution
17923 18012	Data_Sync issue on PID_2 block between backup and syscon	Added the missing state that was causing this discrepancy on PID_2. This was also an issue for PID,PID_SAMPLE,PID_DB "TRUE"

## UNSUPPORTED BLOCKS:

The following blocks are available in the Template and GAP Editor, but are not supported for customer use in the 5.04-3 release version of the Coder (compiler):

- [CAL\\_ID](#)
- [I\\_TO\\_NAN](#)
- [PC104\\_ATL](#)
- [TEMP\\_HIST](#)

## PART NUMBERS:

Version 5.04-3  
Gap/Coder Part Number: 9927-2131 B

## Coder 5.04-3 Release Notes

### DATE:

August 21, 2015

### BACKGROUND:

Version 5.04-3 is a new release of the GAP Programmer / Operating System for the MicroNet and MicroNet TMR control

### COMPATABILITY:

**This version of the GAP Programmer may only be used for MicroNet Plus and MicroNet TMR controls. Atlas, Engine, easYgen and Cyber MicroNet Plus controls are not supported**

To use this version of GAP/Coder, you must use GAP 2.18F or higher. *GAP 3.09 is recommended.*

To use this version of GAP/Coder, you must use SOS 4.01 or higher.

To use this version of GAP/Coder, you must use Control Assistant 4.00 or higher.

To use this version of GAP/Coder, you must use AppManager 3.07 or higher.

To use this version of GAP/Coder, you must use Toolkit 3.5 update 3 or higher.

To use this version of GAP/Coder, you must use Windows XP®, Windows Vista®, Windows 7® or Windows 8®.

### NEW FEATURES:

- **Issue solutions only**

### ISSUES:

The following Product Issue Database ("PID") issues and requests were addressed in 5.04-3:

Issue #	Description	Solution
17888 16982 15919 15910 15502	MPlus backup cpu different values than backup. DIG_FILTER incorrect output on backup cpu. RST on AO_RM_DVR inconsistent on backup cpu. AO has wrong value on backup cpu. AO_RM_DVR incorrect function on backup cpu.	Only run DIG_FILTER and redundancy calculation code on syscon cpu and send <i>all</i> results to backup cpu for data synchronization
17636	Tunable strings not being transferred to backup cpu.	Corrected the arbitrary check which filtered out timer and string values from being transferred to the backup cpu
16858	RTCnet incorrectly appears to be running when the gateway ethernet cables are removed	Added code to call UnLoadCanBuffer() only if RmtChassis is healthy. We identify which RTN chassis port is in use by RTNCanInRmtChasNum[]
16833	Error saving a file (e.g. NV_LOG) on the MicroNet Plus	This occurred because "œ3" was being recognized as an invalid file handle although it is in fact valid and was sometimes used. Removed this incorrect error check
15415	When both of the fault inputs of the AO_2_RM block go true and are reset, the outputs latch and do not return to false on a RST positive edge as expected.	Added Notes in the Block help explaining the behavior of the two fields (SD_AO_2 and ALM_2) in the AO_2_RM block
15414	Inconsistent AO_2_RM output behavior, depending on rategroup	Added Code to reset switchover Delay in AO_2_RM Block, also changed code to make switchover delay time Rate group specific
14432	If the reset input field of a B_ALARM block is set constant TRUE, it does not automatically reset after the trigger is removed.	Added Code to generate reset code if RST field in B_ALARM block is constant "TRUE"

### UNSUPPORTED BLOCKS:

The following blocks are available in the Template and GAP Editor, but are not supported for customer use in the 5.04-3 release version of the Coder (compiler):

- [CAL\\_ID](#)
- [I TO NAN](#)

- [PC104\\_ATL](#)
- [TEMP\\_HIST](#)

## PART NUMBERS:

Version 5.04-3  
Gap/Coder Part Number: 9927-2131 B

## Coder 5.04-2 Release Notes

### DATE:

April 11, 2014

### BACKGROUND:

Version 5.04-2 is a new release of the GAP Programmer / Operating System

### COMPATABILITY:

**This version of the GAP Programmer may only be used for MicroNet Plus and MicroNet TMR controls. Atlas, Engine, easYgen and Cyber MicroNet Plus controls are not supported**

To use this version of GAP/Coder, you must use GAP 2.18F or higher. *GAP 3.08 is recommended.*

To use this version of GAP/Coder, you must use SOS 4.01 or higher.

To use this version of GAP/Coder, you must use Control Assistant 4.00 or higher.

To use this version of GAP/Coder, you must use AppManager 3.02 or higher.

To use this version of GAP/Coder, you must use Toolkit 3.5 update 3 or higher.

To use this version of GAP/Coder, you must use Windows XP®, Windows Vista® or Windows 7®.

### NEW FEATURES:

- **Issue solutions only**

### ISSUES:

The following Product Issue Database ("PID") issues and requests were addressed in 5.04-2:

Issue #	Description	Solution
15470 16478	NetSim does not properly reflect the security model of the runtime system.	Copied the security code from the runtime system. Created a fixed set of accounts (1-15) to represent account-based security models.
16264 16371	MicroNet Plus Backup CPU failure to boot.	This issue was fixed in version 5.04-1 of the coder. It uses functionality in the footprint to automatically recover from a DMA fault. Footprint #5418-2556J is required.
16533	MicroNet I/O Lock when tuning large CURVE_2D_S blocks. Too much time was spent tuning special case curve values with interrupts disabled.	Reduced scope of code where interrupts are disabled during the tuning task.

Issue #	Description	Solution
16963	The <a href="#">DATA_LOG_M</a> block allocates far more memory than required in its configuration space. This could result in subsequent memory violations.	Corrected the pointer arithmetic to allocate the correct amount of memory for DATA_LOG_M configuration.
17171	On-line changes erroneously enabled.	Caused the coder to exit when on-line changes are requested through the SYS_INFO block.
17284	<p>There was an issue discovered during an FAT in Japan where removing an HDDIO module from a redundant MicroNet Plus system during operation caused the backup CPU to fail. The Syscon CPU continued to run while the backup CPU displayed a Watchdog fault.</p> <p>The log.txt file from the Syscon CPU stated:            Â "VxService:CPU: SysCon - Status: Backup CPU failed -- V2"</p> <p>The log.txt file from the Backup CPU stated:            Â "Status: Backup Failed -- SysCon not Communicating"</p>	New IO Module specific functions were added for 32-bit VME access and all 32-bit accesses to the HDDIO module were modified to use the new functions.
15407 15859 15870 16003 16207 16217 17064 17088 17152 17211 17258 17281 17283	Various problems in Block Help topics	Corrected the mistakes in the Help topics

### UNSUPPORTED BLOCKS:

The following blocks are available in the Template and GAP Editor, but are not supported for customer use in the 5.04-2 release version of the Coder (compiler):

- [CAL\\_ID](#)
- [I TO NAN](#)
- [PC104\\_ATL](#)
- [TEMP\\_HIST](#)

### PART NUMBERS:

Version 5.04-2  
 Gap/Coder Part Number: 9927-2131 A  
 Master Kit: 9927-1333, 8928-1088, 1796-1068

### Coder 5.04-1 Release Notes

**DATE:**

April 17, 2013

**BACKGROUND:**

Version 5.04-1 is a new release of the GAP Programmer / Operating System

**COMPATABILITY:**

**This version of the GAP Programmer may only be used for MicroNet Plus and MicroNet TMR controls. Atlas, Engine, easYgen and Cyber MicroNet Plus controls are not supported**

To use this version of GAP/Coder, you must use GAP 2.18E or higher. *GAP 3.06 is recommended.*

To use this version of GAP/Coder, you must use SOS 4.01 or higher.

To use this version of GAP/Coder, you must use Control Assistant 4.00 or higher.

To use this version of GAP/Coder, you must use AppManager 3.02 or higher.

To use this version of GAP/Coder, you must use Toolkit 3.5 update 3 or higher.

To use this version of GAP/Coder, you must use Windows XP®, Windows Vista® or Windows 7®.

**NEW FEATURES:**

℞• **Issue solutions only**

**ISSUES:**

The following Product Issue Database ("PID") issues and requests were addressed in 5.04-1:

<b>Issue #</b>	<b>Description</b>	<b>Solution</b>
15501	AO_RM_DVR causes a compiler error when located in the TMR expansion chassis	Corrected multiple code errors
15666	ISA Firewall can block SOS Servlink OPC Server from communicating with NetSim on the LocalHost channel (127.0.0.1).	Changed the syntax of the TCP write command which NetSim uses for Servlink
15734 15832	In Coder 5.04 and 5.05, it is possible to get the message "RD_SEC must be less than or equal to the WR_SEC" for a read value	Changed to make only Input Fields care about write security
15937 15997	TMR 5200 cpu fault when calibrating actuators	Synchronized actuator value updates during calibration
16109	HDDIO modules fault on failover in expansion chassis	Updated RTN chassis with rategroup information to allow proper synchronizing
16204 16654	MicroNet TMR kernel trip due to communication problem	Fixed issue by sharing time between the network task and the RTN task to allow rendezvous to occur during high network loading  NOTE: This fix requires footprint version 5418-2918D
16620	Some large applications have a compile error because of an undeclared variable "œtus3" in a split-up function	Fixed the code generation to declare the variable in all used code sections
16683	Problem showing Servlink TOD values in NetSim	The specialized Servlink handle for TOD values was not understood by the NetSim code. The handle was changed to a standard handle

Issue #	Description	Solution
16644 15909	Tuning input values on MOD_PORT or MOD232PORT blocks connected to an SIO module can cause a TMR CPU to fail	MOD_PORT and MOD232PORT blocks are no longer allowed for SIO modules, use SIO_PORT and SIO232PORT blocks instead
16781	Data Coherency Issue could potentially cause TMR CPUs to read the incorrect synchronization value and fail	Fixed issue by changing the order of writes for the synchronization value
16787	Some applications were using more than the maximum number or 10000 HMI Points	Increased the maximum to 100000

## UNSUPPORTED BLOCKS:

The following blocks are available in the Template and GAP Editor, but are not supported for customer use in the 5.04-1 release version of the Coder (compiler):

- [CAL\\_ID](#)
- [I\\_TO\\_NAN](#)
- [PC104\\_ATL](#)
- [TEMP\\_HIST](#)

## PART NUMBERS:

Version 5.04-1  
 Gap/Coder Part Number: 9927-2131 NEW  
 Master Kit: 9927-1333, 8928-1088, 1796-1068

## Coder 5.04-0 Release Notes

### DATE:

April 4, 2011

### BACKGROUND:

Version 5.04-0 is a new release of the Coder / Operating System

### COMPATABILITY:

To use this version of GAP/Coder, you must use GAP 2.18D or higher. *GAP 3.03 is recommended.*

To use this version of GAP/Coder, you must use SOS 4.00 or higher.

To use this version of GAP/Coder, you must use Control Assistant 4.00 or higher.

To use this version of GAP/Coder, you must use AppManager 3.0 or higher.

To use this version of GAP/Coder, you must use Toolkit 3.5 update 3 or higher.

### NEW FEATURES:

**Direct Memory Access in J1939:** The [J1939\\_DMA](#) block was developed to enable custom building of service tools which communicate over CAN.

**Pattern 23 implementation for PCM128-HD platform**

**AI\_PWM DG3 measurement below 17Hz:** DG3 measurement lowered from 17Hz to 5Hz by using a 24-bit accumulator



**PCM128-HD Model Year 2007 depopulation requirements:** Changes were made according to anticipated specifications. However, no testing has been performed because the unit is not yet available.

## ISSUES:

The following Product Issue Database ("PID") issues and requests were addressed in 5.04-0:

Issue #	Description	Solution
2437 4634	ECM3 AO_PWM_FLT intermittent open faults using DO_5, DO_6, DO_11 and DO_12	This is normal behavior for the hardware when used as a PWM output with an open circuit or a short circuit on the load
2531	PCMHD - PID_ENGA does not work correctly at RAM Optimization Level 2	PID_ENGA was added to the list of blocks which are excluded from optimization
4634	See issue 2437 above	
9093	MCU CANOpen (SDO) Serial Number changes are not always saved	Properly initialized an array used by the serial number code
13295	Incorrect ignition timing on PCMHD (fires last cylinder late upon sync)	Fixed by not enabling fuel spark until synchronized
13859	EFI_P_CORE doesn't distinguish noise spikes from proximity sensor pulses	Pattern 12, 18 and 19 were modified to reject cam pulses less than 100µs. This applies to both prox and mpu inputs. The mpu input is converted to a pulse that is as wide as the points between the zero crossing
14119	Pattern 21 does not support more than 127 teeth	Pattern 21 and Pattern 22 together are used to service a range of teeth between 60 and 200
14203	Incorrect units and access in CAL_CURVE3 curve limit function	These bugs were corrected without documentation in version 5.03-0. The fix was verified in this version
14339	Duplicate loaded values can be wrong in tunable upload	Fixed by disallowing multiple aliases to tunable values in the Coder. If an alias (HMI_PT or QSERV_HDR) exists for a tunable value, no Debug entry is created for it.  Also, it is no longer permitted to have multiple HMI_PT or QUICK_SERV aliases for the same tunable. GAP 3.02 and above can monitor these values correctly, but GAP 2.x doesn't know how to look them up by their HMI_PT name, so they are displayed as unavailable.
14439	TMR VxWorks Footprint new MicroNet I/O module functionality	Added functionality to program MicroNet I/O modules Fixed NAT issue and network stack priority  <i>Part number 5418-2918 C</i>

Issue #	Description	Solution
14520	Knock ratio glitches in slow rate groups	Woodward recommends running this block at rategroup 5
14605	Delete code for RATIOLIM2 block	The obsolete code has been removed
14606	ACT_CTRL can be calibrated illegally on the backup CPU	Only the SysCon cpu may change the ACT_CTRL calibration. The SysCon cpu now passes some state information to the backup which allows the backup to reject such calibration requests.
14646	AI_MPU_ENG misfire priorities are not properly set when other AI_MPU_ENG blocks are enabled or disabled	Fixed some AI_MPU_ENG block enablement errors which were upsetting the priority scheme
14654	MISFIRE output reads non-zero when disabled	Fixed together with fix for issue 14646
14655	DIG_FILTER name inputs	Added support for block names to the FREQ and Q_FACTOR fields
14766	DATA_LOG_M problems in applications with a MATLAB block, but not all rategroups represented	The MATLAB block was causing the DATA_LOG_M logic to assume that a rategroup function existed for each rategroup. The logic was updated to properly evaluate the existence of each such function before attempting to call it
14861	Coder crashes when there are special HMI_PT block in the application (like MOMNTARY_B), but no "normal" HMI_PT blocks	The SID building code of the Coder was trying to close a namespace which had never been opened by an HMI_PT block. Corrected the decision to close the namespace
14874	Runtime crash when DATA_LOG_M block is defined with no variables to log	Corrected a possible divide by zero exception in the Coder
14876	CAL_CURVE2 compiler crash when filename is a number	The Coder now checks for invalid filenames and returns an error instead of trying to use the name
14965 15110	TMR CPU not grouped in AppManager after hotswap	When a CPU fails or is re-synced, all CPUs will set the current Control ID name. This ensures a CPU with a different name will set the correct name when it first comes into Sync
15024	5200 RTN CPU restart problem after RTN fault	Made RTN CPUs with a fault status respond with a failure when asked if running. This prevents the main CPU from trying to start an application on them

Issue #	Description	Solution
15075	CAN Tx errors on message conflict	<p>This problem was diagnosed as follows: <i>Multiple controls were sending the same message (identical CAN IDs). As the clocks drift, eventually 2 will try to send the same message at exactly the same time. This violates the CAN standard but isn't prohibited in CANOpen.</i></p> <p>Woodward recommends modifying applications to avoid simultaneously sending the same CAN ID from multiple controls</p>
15110	See issue 14965 above	
15138 15215	AppManager file modified time is wrong after retrieving or after datalog collection (TMR only)	<p>Missing FTP functions were added to the TMR footprint <i>Part number 5418-2918C</i></p>
15242 15375	MicroNet TMR Reboot issue	<p>The three CPUs were having trouble finishing all of their startup tasks on the same schedule. Various tasks were moved to the post-boot code so that the CPUs could remain synchronized</p>
15283	After loading an application for the first time into a green PCM-HD, the controller does not reset and power must be cycled	Fixed by updating the MotoHawk boot code
15314	Lost CAN messages	<p>Added message filtering/sorting so that overhead messages don't flood the CAN port causing important messages to get dropped through lack of processing time</p>
15326	DIG_FILTER B_ENABLE field doesn't work	Added standard B_ENABLE logic to the DIG_FILTER block
15332	DVP blocks use wrong variable	Corrected the variable
15373 15374	Remove CO_IC1100Counter	Removed the obsolete variable
15375	See issue 15242 above	
15406	MCU / ECM3 PWM_IN cyclic fault problem when moving from a frequency below 430Hz to ~470-500Hz	<p>Modified TPU code to use a 24-bit accumulator, allowing use of the 28Mhz clock over the entire frequency range in the MCU and ECM3. AI_PWM can now read down to 4Hz</p>
15411	MCU Speed sensor input failure	This problem was fixed by correcting the misfire enablement code

Issue #	Description	Solution
15418	PCM_KNOCK Toolkit tables missing	Re-instated code which was inadvertently commented out in version 5.03-0
15424	CO_IC1100 block not processed	A variable which was required for recognizing the CO_IC1100 block was inadvertently reset. This mistake was corrected.
15426	hmi_pt.txt file does not include HMI_ENUM info	The HMI_ENUM block does not need to be represented in the hmi_pt.txt file. The hmi_pt.txt file was replaced by the hmi_pt.xls file in this version. The hmi_pt.xls file was carefully designed and verified in this version to handle all HMI_PT values appropriately
15427	PCMHD HV AN_33 and AN_34 do not function correctly	Inputs AN_33 and AN_34 have 1K Ohm pull ups to 5Vtts. The test stand had a 499k resistor to ground and was fed with a current source. That is why it looked like they were not working. These inputs must be fed by a source with a high impedance
15430	If the SD input on the PCMHD H_BRIDGE block is a constant false, the output is SD during the init process and the High side never gets re-enabled in the rategroup. You will only be able to drive a negative duty cycle	Added code that sets the SD output false if it has a constant false value
15431	HMI_PT security level does not override the default read security values from the SYS_INFO block	Added the missing security transfer code to these values
15432	In some cases the ALM_NO output of the TCHAS_STAT block can be different between CPUs	The code which diagnosed alarm conditions was corrected to read from the requested CPU
15435	Block Help shortcut is wrong in 5.03 install	Corrected shortcut for subsequent installs by creating a variable representing the version labeling string. The variable is used for all files and shortcuts which use a version string
15444	Late pulse in IC1100SPRK	Adjusted dwell durations to allow the IC-1100 to detect the late pulses as valid ones

Issue #	Description	Solution
15449 15597	TMR CPU voting error	Corrected a problem in the voting logic with data packets of over 400 bytes
15450	An issue was found in the mios bios code for the 565 that introduces a 1 rategroup delay whenever the duty cycle switched from a duty cycle between 0 and 100 to either 0 or 100. This was causing some unwanted latency in an OH control loop.	Corrected a problem scheduling the setting of "transparent mode".
15459	J1939 network error when there are no write blocks	J1939 incorrectly sizes the Tx fifo size when there are no write messages defined in the application. There must be 2 entries for the fifo code to work correctly. Corrected the fifo sizing code to take this into account when adding the address claim message space.
15463	Problem with math.h include file for NetSim	Added math.h file back into installation of sim504 folder
15465	Coder 5.03 not compatible with GAP 2.18D	Coder was checking for the literal string "GAP 2.18C". Corrected Coder to compare the string ordinally to see that "GAP 2.18D" is "higher" than "GAP 2.18C"
15469	ACT_CORR file mismatch error on TMR control reset	Synchronized the startup code to set the time variable at an appropriate time
15472	Do not allow J1939 in the MicroNet	J1939 regression testing on the MicroNet was consuming too much time. We established that no one was using J1939 on the MicroNet and then disabled the functionality on that platform
15473	If the ACT_CTRL NULL_I is a constant, the Coder interprets it as 0 no matter what the value is	Corrected the constant loading code for the ACT_CTRL block
15476	NODE_ID input fields should be mandatory	Set the NODE_ID fields to Mandatory for the following blocks: CO_TECJET, CO_DVP, CO_SDO, CO_SPC, CO_VPC, COPEN_NTW, J1939_NTW, J1939_TJET
15477	CO_TECJET DES_FLOW and CALC_FLOW are off by a factor of 3.6	Corrected the logic to divide by 3600 instead of 1000 (as per specification)

Issue #	Description	Solution
15494	Wrong units for CAL_CURVE2 Z_TABLE in .sid file	Corrected logic to only override the block units if there is a curves file entry in the z table
15531	The HELP_FILE/H_MSG feature doesn't work in PCMHD	Corrected some logic errors in parsing the HELP_FILE which was preventing this function from working
15549	Problem compiling LinkNet in TMR in NetSim	Fixed an indexing problem in the simulation code
15553	J1939 Asynchronous CAN write failure	Added code to ensure that destination addresses are verified before transmitting a message
15561	PASSWORD block has wrong level range and help fieldnames are wrong	Changed range from 1..16 to 1..15.Â Corrected Help file
15583	PCMHD TACH block PULSES template range doesn't match Coder	Changed the template range to 1..16 to match what the Coder allows
15584	A_CURVE3 LD_DFLT should be edge-triggered	A bracket was inadvertently removed from the code in a previous Coder version.Â The bracket was replaced to correct code scoping
15587	J1939_RDMP block should be run in the same rategroup as the network block	Added a Coder check which requires this behavior
15591	NetSim can't call IO_LOCK in Coder 5.03	Security was added to version 5.03, which prevents commands like IO_LOCK from being called without proper authority.Â In 5.04-0 (and in the latest CoderSim for 5.03-0), NetSim is granted full authority (level 15) for all commands and data access
15597	See issue 15449 above	
15598	DVP over CAN does not work in MicroNet RTN chassis	Created a larger RTN CAN move buffer and changed scheduling such that RTN_CPU only moves CAN messages to the transmit buffer when the fastest rategroup is scheduled

Issue #	Description	Solution
10053	Gap Block Help Issues	Modified 5.04 Block Help
11692		
14208		
14339		
14422		
14595		
14618		
14693		
14704		
14761		
14763		
14976		
14994		
15317		
15420		
15435		
15448		
15457		
15466		
15475		
15478		
15514		
15561		

### UNSUPPORTED BLOCKS:

The following blocks are available in the Template and GAP Editor, but are not supported for customer use in the 5.04-0 release version of the Coder (compiler):

- [CAL\\_ID](#)
- [L\\_TO\\_NAN](#)
- [PC104\\_ATL](#)
- [TEMP\\_HIST](#)

- Tunable inputs for the [SYNC\\_INJ](#) block are only available to Woodward developers in this version

### PART NUMBERS:

Version 5.04-0  
 Gap/Coder Part Number: 9927-1909  
 Master Kit: 9927-1333, 8928-1088, 1796-1068