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| **GAP Coder 4.05-0**  9927-1545  Release Notes06/3 | |
| **Description** | Version 4.05-0 is a new release of the Coder. |
| **Enhancements** | 1) Added J1939 BAM messaging  2) EG3000 changes  3) Freed up Application RAM on OpWgx platform  4) MISFIRE changes  5) Added J1939 CURVES  6) SYNCHRO changes  7) Atlas II changes |
| **Product Issue Database(PID) Issues Resolved** | **PID Issue # 2562: doprint() bug with Datalog.**  Problem: If the datalog block contains exponential notation values, the MCU crashes on retrieval.  Solution: Fixed arg list for exponential values.  **PID Issue # 5352: Synchro updates.**  Problem: The Synchro updates concerning Dwell time made in 4.03 have not been added to the 4.04-0 software.  Solution: Updated 4.05-0.  **PID Issue # 4751: DELAY block bug.**  Problem: With the DLY\_TIME input set to a constant 0 and the NEG\_DTIME set to \*2 if you set the trigger TRUE then FALSE, wait for the DELAY output to go FALSE, then tune the NEG\_DTIME up to 10 the R\_TIME output will increase above zero. The DELAY output remains FALSE.  Solution: Coder file blk\_gen.c was changed to ensure the internal timer values is locked to the value of NEG\_DTIME when the timer has expired.  **PID Issue # 5493: LATCH\_AE bug.**  Problem: If 1) IN\_2, IN\_3 then IN\_1 are set true 2) IN\_2 set false 3) FA\_RST then RST set true FIRST\_ALARM goes to 0 4) FA\_RST set false FIRST\_ALARM stays 0 5) IN\_3 set false FIRST\_ALARM goes to 1.  Solution: The code was changed so the value of FIRST\_ALM always goes to the value of the oldest alarm when the RESET input is TRUE.  If there is no alarm the output will be zero.  **PID Issue # 6554, 7091: DM1 bug.**  Problem: DM1 messages stop when more than 1 message is active.  Solution: The location of the DM1 code was placed at the start of the RG task to assure consistent timing.  **PID Issue # 6934, 6991, 6992, 8273, 8331: Atlas II bugs.**  Problem: The SmartCore A5200 AI\_420\_ATL GAP block does not currently sense hardware faults from the status byte in the Analog/Digital converter data. If an Atlas-II GAP application that uses the SmartCore A5200 actuator or analog outputs is executed, then a new application is loaded and run that does not use those actuator/analog outputs, the analog outputs will remain set to the same position as last set in the original (first) application. The new Atlas-II EBX Combo module speed derivative is not the same as the AtlasPC.  Solution: A FAULT output has been added to the AI\_420\_ATL block. The software was changed to always initialize all FPGA PWM outputs to zero before the GAP application program is started. Changed the Speed Derivative formula to match.  **PID Issue # 5951, 3763: Footprint Issues.**  Problem: When starting an application with multiple expansion chassis not all of them will reliably start.  Solution: The VxWorks operating system has had the NUM\_SYS\_64,128,256 and 512 buffers increased from 64 to 256. |
| **Supported Platforms** | The following Hardware Platforms are supported in the 4.06-0 release:  **MicroNet Plus**  **EasyGen 3000**  **MCU**  **InPulse II**  **ECM3**  **PCMHD**  **Atlas II** |
| **Miscellaneous PID Issues Completed** | 2301, 3845, 5432, 5795, 6711, 7271, 6531, 2420, 6591, 5972, 7832, 8111, 7031, 6292, 2483, 5111, 5371, 8071, 5532, 5792, 6031, 6051, 6893, 6974, 7511, 7092, 2536, 7011, 7211, 7332, 7612, 7451 |
| **Customer Action** | If the new release is critical for your application, you may download the software from the [www.woodward.com](http://www.woodward.com) web site. You can also contact Woodward and order Kit# 8928-1088L and get the software plus additional information in the form of a DVD.  If more Information is needed, please contact Woodward’s Technical Assistant Group at the phone numbers listed below. |

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