MicroNetTMR 1.01-2 Release Notes

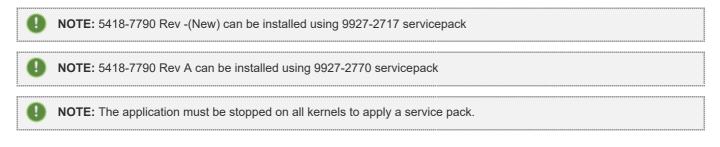


DATE:

December 07, 2020

BACKGROUND:

MicroNetTMR GAP Programmer 1.01-2 is a new release of the Coder for MicroNet TMR controls. It was developed from MicroNetTMR GAP Programmer 1.01-1. Please see below for release notes



COMPATABILITY:

Only 5466-1347 and 5466-1350 versions of the TMR CPU are supported in this coder release. Footprint 5418-7790.

- To use this version of GAP/Coder, you must use GAP Editor 4.01-1 or higher or GAP Editor 3.14-2 or higher
- To use this version of GAP/Coder, you must use SOS 4.15 or higher.
- To use this version of GAP/Coder, it is recommended to use AppManager 3.14 or higher.
- To use this version of GAP/Coder, it is recommended to use Control Assistant 4.14 or higher.
- To use this version of GAP/Coder, you must use Toolkit 5.1 or higher.

NOTE: It is recommended to use the latest versions of all software tools.

ISSUES ADDRESSED IN CODER TMR 1.01-2

- Issue#106432,106430 : ACT_CTRL calibration getting lost on expansion chassis
- Issue#92228 : TMR kernel goes into inactive if COPEN_NTW.CAN_NTW_EN is different on the running system
- Issue#90570 : SAMP_ITUNE Sampling incorrectly
- Issue#103843 : Channel Faults occur on using 5466-5025 MicroNet 24/8 Analog I/O Smart-Plus Module on Expansion Chassis
- Issue#67785 : {PID#16952}generate a warning if RCV_BUF_SZ may be set to low on COPEN_NTW
- Issue#64864 : EE file can be corrupted when doing back 2 back saves from servlink
- Issue#64874 : {PID#17802}Latch block not responding when RST is Constant FALSE
- Issue#106442 : Inconsistent data flow with CAN_MSG blocks
- Issue#103832 : Incorrect data type casting of Device Channel (port number for UDP)
- Issue#67538 : {PID#15462}ACT_CORR error behavior with TMR controls
- Issue#90554 : Block Help Change : Special note nvlog file can set EE_BUSY flag
- Issue#67335 : Block Help Change : Total Load spike when syncing third kernel into a running system
- Issue#78417 : Block Help Change : Repeat group value on momentary_b block was incorrect

This version of the Coder / Template is dedicated to the MicroNet TMR platform only. The Product Release Strategy has evolved so that other platforms such as MicroNet Plus, AtlasII, 2301, and LECM each have their own dedicated

UNSUPPORTED BLOCKS:

The following blocks may be available in the Template and GAP Editor, but are not supported for customer use in the MicroNetTMR 1.01-2 release version of the Coder (compiler):

- RT_CAN_NTW
- CUSTOM_CAN
- CO_IC1100
- CO_TECJET
- CAL_ID
- I_TO_NAN
- WGSPL
- MATLAB_LINK

PART NUMBERS:

MicroNetTMR 1.01-1 Release Notes

WOODWARD

DATE:

January 31, 2020

BACKGROUND:

MicroNetTMR GAP Programmer 1.01-1 is a new release of the Coder for MicroNet TMR controls. It was developed from MicroNetTMR GAP Programmer 1.01-0 to address critical Issues. A newer version of Footprint, 5418-7790 Rev A, was also released. There are no compatibility issues between the coder and the older footprint versions. Please see below for release notes for coder and the footprint.



NOTE: The application must be stopped on all kernels to apply a service pack.

COMPATABILITY:

Only 5466-1347 versions of the TMR CPU are supported in this coder release. Footprint 5418-7790.

- To use this version of GAP/Coder, you must use GAP Editor 4.01-1 or higher or GAP Editor 3.14-2 or higher
- To use this version of GAP/Coder, you must use SOS 4.15 or higher.
- To use this version of GAP/Coder, it is recommended to use AppManager 3.14 or higher.
- To use this version of GAP/Coder, it is recommended to use Control Assistant 4.14 or higher.
- To use this version of GAP/Coder, you must use Toolkit 5.1 or higher.

NOTE: It is recommended to use the latest versions of all software tools.

ISSUES ADDRESSED IN CODER TMR 1.01-1

- Issue#87504 : RATIO_LIM2 errors when re-syncing a TMR CPU
- Issue#64924 : ALM_RST on ACT_CTRL module was not edge triggered when used in an expansion chassis
- Issue#71224 : Possible memory error during application compile for SIO module
- Issue#69987 : Memory not being returned by the system for SIO module on application stop causing excessive memory usage.
- Issue#69996 : LinkNet HT Reboot network / node when NODE_ID is connected slower rate group GAP blocks.
- Feature#80159 : Additional information added to "TMR app too big" error coder message.

ISSUES ADDRESSED IN THE FOOTPRINT 5418-7795 REV A

- Issue#80156 : VxWorks Urgent/11 Vulnerabilities
- Issue#71221 : Sending tunables to control causes TMR CPU to go into inactive
- Issue#84676 : Control information change for 5466-1347 CPU
- Issue#69982 : WindRiver patch for vxWorks 6.8.3 to prevent possible CPU reset. See WindRiver VXW6-8588 issue
- Issue#67798 : Loading a DataLog Configuration File caused an EGD communication failure
- Issue#65166 : Prevent MicroNet Plus EP/Cyber service pack from installing on a non EP/Cyber CPU
- Issue#67081 : Copying or deleting of .out/.ee/.r1/.r2/.r3/.r4/.r5/.r6/.r7 is logged in log.txt
- Issue#76444 : Health Monitor task data access exception while downloading files using multiple SFTP clients.
- Issue#65168 : Amservice(vxService) ethernet resource allocation not allowing new connections after several disconnections.
- Issue#76457 : Do not allow the files in OS directory to be renamed/edited with root lock enabled.
- Feature#80146 : Add AppManager compatibility check in footprint
- Feature#80151 : Production request : Change the default IP's in the Woodward Production system.
- Feature#67210 : Add option to save WindView log to a custom path
- Feature#67329 : Add time stamps in System log files
- Feature#73440 : Recognize legacy modules in AppManager I/O module show function.

ISSUES FIXED IN PREVIOUS TMR 1.XX RELEASES BUT WAS NOT RECORDED DUE TO BUG TRACKING SERVER MIGRATION ISSUES

TMR 1.01-0

- Issue # 69342 : In some configurations the last SIO_PORT in the SIO module may be in a FAULT state
- Issue # 67249 : Memory corruption possible when the SERVLINK block is used and the application name is longer than 19 characters.
- Issue # 67487 : LN_RTD LAG_TAU input not functioning
- Issue # 67371 : Undefined Symbols when Ladder Logic blocks are in the application
- Issue # 69991 : Cannot have CO_SPC blocks on more than 1 CAN network
- Issue # 67298 : LN_RTD block field help update.
- Issue # 67271 : T_3_RM MAXDIFF_X outputs not working
- Issue # 67605 : ACT_CTRL Block Help Changes

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- Issue # 67913 : CURVES_2D_S Calculation different when using a file input
- Issue # 67323 : TMR Failure to re-sync when using very large applications.

Issue # 67276 : Remove PGX-specific fields from HMI_PT block

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UNSUPPORTED BLOCKS:

The following blocks may be available in the Template and GAP Editor, but are not supported for customer use in the MicroNetTMR 1.01-0 release version of the Coder (compiler):

- RTN_DIR
- RT_CAN_NTW
- CUSTOM_CAN
- CO_IC1100
- CO_TECJET
- CAL_ID
- I_TO_NAN
- WGSPL
- MATLAB_LINK

PART NUMBERS:

Version 1.01-1 Gap/Coder Part Number: 10-011-218 Release Notes: 10-011-219 Can be found on **→ www.woodward.com/software**

MicroNetTMR 1.01-0 Release Notes

DATE:

February 20, 2019

BACKGROUND:

MicroNetTMR GAP Programmer 1.01-0 is a new release of the Coder for Micronet TMR controls. It was developed to support CANOpen in the MicroNet TMR system.

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COMPATABILITY:

Only 5466-1347 versions of the TMR CPU are supported in this coder release. Footprint 5418-7790. 9927-2717

NOTE: Service Pack 9927-2717 may be used to convert the footprint.

- To use this version of GAP/Coder, you must use GAP Editor 4.01-1 or higher or GAP Editor 3.14-2 or higher
- To use this version of GAP/Coder, you must use SOS 4.15 or higher.
- To use this version of GAP/Coder, it is recommended to use AppManager 3.12 or higher.
- To use this version of GAP/Coder, it is recommended to use Control Assistant 4.14 or higher.
- To use this version of GAP/Coder, you must use Toolkit 5.1 or higher.

NOTE: It is recommended to use the latest versions of all software tools.

NEW FEATURES:

CAN OPEN FOR MICRONET TMR SYSTEMS

- CAN Open devices such as LinkNet HT, DVP, SPC, and standard CAN Open read / write are supported among other CAN Open functions.
- The MicroNet TMR supports independent CAN Open ports on each Kernel CPU. The COPEN_NTW block is supported for these ports. These ports are not redundant. See MicroNet TMR Can.
- STANDBY input field on LinkNet node blocks. See Redundancy for LinkNet HT
- RTCNet is NOT supported
- CAN on RTN Expansion chassis is NOT supported
- RTN Gateway is NOT supported

FOOTPRINT CHANGES

- This footprint has Achilles Level 1 certification.
- Support storing of GUI application on the control for the Qt Remote View tool

ISSUES ADDRESSED

- Issue # 60531 : TMR: All kernel C IO_FLT output go true when CPU A is faulted
- Issue # 65165 : Appmanager Shows incorrect status
- Issue # 64695 : TMR RG 5 I/O Intermittent
- Issue # 64697 : TCP Modbus not working in MicroNet TMR
- Issue # 64730 : MODBUS_M PORT_2 timeout problem
- Issue # 66067 : PID_OPTI issue when CPU is re-synced
- Issue # 64828 : More useful MicroNet TMR Fail Diagnostics
- Issue # 64768 : Tunable timeout and SOS reset
- Issue # 63054 : RTN communication
- Issue # 64774 : Loading tunables/tuning values on curve blocks does not save
- Issue # 64765 : Linknet HT nodes not initializing when one COPEN_NTENABLE is FALSE
- Issue # 64825 : CO DVP slow message timeout doesnt monitor all slow messages
- Issue # 65167 : Log new application loads and unloads in log.txt
- Issue # 64855 : Tunable Out of Range Not Loading
- Issue # 64714 : Daylight Savings Time (DST) Option on RTC Block Nonfunctional
- Issue # 64931 : ACT_CTRL GAP block FB_POS_TOL & FB_DIF_TOL max value less then 200
- Issue # 65054 : EXP_DBG code can cause memory corruption
- Issue # 64683 : Coder crashes on long driver name in the DIG_RVLV block
- Issue # 64699 : EGD Consumer DLY_TIME
- Issue # 64713 : LN_CJ is missing in GAP help: Fix in TMR1.0X
- Issue # 64698 : LinkNet HT TC module won't communicate when FAIL_MODE = LOW
- Issue # 64948 : CO_DVP Update DVP TxPDO's
- Issue # 64923 : PID_OPTI fix
- Issue # 65167 : Log new application loads and unloads in log.txt
- Issue # 66979 : system log files with extensions .txt.old
- Issue # 64855 : Tunable Out of Range Not Loading
- Issue # 64714 : Daylight Savings Time (DST) Option on RTC Block Nonfunctional in MicroNet CDR 1

- Issue # 65054 : EXP_DBG code can cause mr_i corruption (expansion chassis)
- Issue # 64683 : Coder crashes on long driver name in the DIG_RVLV block
- Issue # 64699 : EGD Consumer DLY_TIME (template help text)
- Issue # 64723 : EGD Consumer TIME_OUT in NetSim (update help)
- Issue # 64713 : LN_CJ is missing in GAP help (update help)
- Issue # 64698 : RTCnet TC module won't communicate when FAIL_MODE = LOW : TMR1.01-0 (default changed in template)
- Issue # 64727 : A_CURVE3 block doesnt accept '-' or part #'s:
- Issue # 64696 : DATA_LOG_M in TMR COLLECTING output goes FALSE (viewed on B) when Kernel A is stopped (update help)
- Issue # 64726 : EGD Broadcast Address Help: Fix in TMR 1.0X (update help)
- Issue # 64733 : AO_FORCE Default View: Fix in TMR1.0X
- Issue # 64776 : GAP Coder 32 bit Integer tunable range
- Issue # 64942 : PROP block output when B_ENABLE = FALSE (update help)
- Issue # 64989 : DATALOG_M help note for ZMINUS1-blocks is inaccurate: (update help)

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- MATLAB_LINK

PART NUMBERS:

Version 1.01-0

Gap/Coder Part Number: 10-011-218 Release Notes: 10-011-219 Can be found on • www.woodward.com/software